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UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :
Shoriki NARITA et al. : Confirmation No. 1853
Serial No. 10/019,700 : Attn: Application Branch
Filed January 2, 2002 : Attorney Docket No. 2001_1876A

BUMP FORMING APPARATUS FOR
CHARGE APPEARANCE SEMICONDUCTOR
SUBSTRATE, CHARGE REMOVAL METHOD
FOR CHARGE APPEARANCE SEMICONDUCTOR
SUBSTRATE, CHARGE REMOVING UNIT FOR
CHARGE APPEARANCE SEMICONDUCTOR
SUBSTRATE, AND CHARGE APPEARANCE
SEMICONDUCTOR SUBSTRATE

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REQUEST FOR CORRECTED FILING RECEIPT

Assistant Commissioner for Patents,
Washington, DC 20231

Sir:

A corrected filing receipt is requested in the above-referenced case. A copy of the original filing receipt is enclosed, with the changes noted thereon.

Expedited issuance of the corrected filing receipt is respectfully requested.

Respectfully submitted,

Shoriki NARITA et al.

By Michael S. Huppert

Michael S. Huppert
Registration No. 40,268
Attorney for Applicants

MSH/kjf
Washington, D.C. 20006-1021
Telephone (202) 721-8200
Facsimile (202) 721-8250
March 20, 2002



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/019,700	01/02/2002	2812	1670	2001-1876A	60	40	8

CONFIRMATION NO. 1853

00513

WENDEROTH, LIND & PONACK, L.L.P.
2033 K STREET N. W.
SUITE 800
WASHINGTON, DC 20006-1021

FILING RECEIPT



OC000000007557719

Date Mailed: 03/13/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Shoriki Narita, Osaka, JAPAN;
Yasutaka Tsuboi, Osaka, JAPAN;
Masahiko Ikeya, Osaka, JAPAN;
Takaharu Mae, Osaka, JAPAN;
Shinji Kanayama, Nara, JAPAN;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/JP00/04280 06/29/2000

F reign Applications

JAPAN 11/189053 07/02/1999
JAPAN 11/293702 10/15/1999
JAPAN 11/308855 10/29/1999
JAPAN 11/323979 11/15/1999
JAPAN 2000-184467 6/20/2000

Projected Publication Date: Not Applicable, filed prior to November 29,2000

Non-Publication Request: No

Early Publication Request: No

Title

Electric charge generating semiconductor substrate bump forming device, method of removing

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electric charge from electric charge generating semiconductor substrate device for removing electric charge from electric charge generating semiconductor substrate, and electric charge generating semiconductor substrate

Preliminary Class

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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